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Study of the effects of annealing temperature on the properties of piezoelectric *ZnO* thin film for the development of MEMS acoustic sensor

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Abstract

This paper reports deposition and characterization of Zinc oxide (ZnO) thin film to be used as sensing layer in an acoustic sensor. An important requirement for such devices is the presence of high c-axis orientation and low stress in the deposited ZnO film. For this purpose, annealing of the deposited films is very much necessary. Here, zinc oxide thin films have been deposited over oxidized Si(100) wafer using radiofrequency (RF) magnetron sputtering method and are annealed at different temperatures from $200-400^{\circ}C$ in steps of $50^{\circ}C$. Different microstructural parameters such as grain size, surface roughness, dislocation density and stress have been determined to obtain the optimized annealing temperature for the thin film. The optimization of the annealing temperature is necessary to obtain ZnO thin film with better piezoelectric response. Surface roughness has also been determined from AFM measurements. The XRD study indicates that the intensity of (002) peak changes with annealing temperature. Grain size increases from 15.46nm to 18.24nm and dislocation density decreases to minimum value $\sim 3.006 \times 10^{-3} nm^{-2}$ as temperature is increased to $400^{\circ}C$. Stress is also minimum at temperatures of $350-400^{\circ}C$. An annealing temperature of approx. $350^{\circ}C$ has been found to be optimum for ZnO film to be used in acoustic sensor.

Keywords: Acoustic sensor, Annealing, ZnO, stress, dislocation density, grain size;

1. Introduction

Most of the micro-electromechanical systems (MEMS)-based acoustic sensors have been developed for audio applications that normally require a maximum SPL of I20dB and bandwidths of 20kHz [1-8]. Among the commonly used transduction mechanisms of capacitive, piezoresistive and piezoelectric, piezoelectric transduction offers advantages of durability, high sensitivity, low noise and also it has no power requirement for its operation [9]. Generally, in MEMS, thin film devices are preferred over their bulk counterparts due to the inherent advantages in device design flexibility and also due to the feasibility of integration in complex systems [10]. In recent years, ZnO has become one of most widely used piezoelectric thin films for a variety of applications in MEMS such as filmbulk acoustic-wave resonators, SAW resonators and acousto-electric devices [11]. Thin film ZnO layers can be deposited by using physical vapour deposition (PVD) methods [12-13], evaporation [14], sol-gel processing [15-16] and metal-organic chemical vapour deposition (MOCVD) [17]. However, among these, sputtering [18-19] have been extensively used to deposit c-axis oriented ZnO layers due to its ease of operation, good repeatability, option of

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deposition at low temperature and compatibility with standard Si- fabrication technology.

Mechanical stress arises in thin films due to its deposition on a substrate. This stress results from two factors—one arises from the differences in coefficients of thermal expansion (CTE) of piezo-electric thin film and the underlying layer and the other one arises from the existence of structural imperfections such as grain boundaries, vacancies etc. in the thin film [11]. However, intrinsic stress arising from the *ZnO* thin film growth process causes the phase velocity in acoustic wave devices to change significantly. Moreover, stressed *ZnO* films exhibit relaxation behavior with time, causing aging effects which alter the specifications of the device [20]. Furthermore, presence of intrinsic stress gives rise to a static transverse deflection even in the absence of an applied pressure which greatly affects the sensitivity of the device [21]. Therefore, deposition of a stress-free or low stress *ZnO* film is essential for acoustic devices. Moreover, reduction in dislocation density or increase in grain size greatly improves the crystallinity of the film which in turn enhances its piezoelectric performance. All of these can be achieved through proper application of post-deposition annealing. Therefore, in the present approach, annealing temperature optimization has been done to obtain minimum stress, dislocation density, increased grain size and enhanced crystallinity of the *ZnO* thin film to be used in an acoustic sensor.

ZnO thin film was deposited using RF reactive magnetron sputtering system using a Zn metal target of 99.99% purity and O_2 supply. The deposition was carried out at room temperature without any substrate heating. The optimized deposition rate was 7nm/min and after 6hrs, a thickness of $2.5 \, \mu m$ was obtained. Table 1 is showing the deposition parameters used for sputtering. Deposited samples were then annealed for 1hr at different annealing temperatures ranging from $200-400^{\circ}\text{C}$ in steps of 50°C in O_2 ambient. The structural and morphological studies were carried out using an X-ray diffractometer (Rigaku) and Atomic Force Microscope (Veeco), respectively. Grain size, strain and stress were calculated from XRD spectrum and surface roughness from AFM image.

| Parameters | Values |
|---------------------------------------|--------------|
| RF power | 400W |
| Working Pressure | 20mTorr |
| Gas flow rate | Ar: 20sccm |
| | $O_2:30sccm$ |
| Substrate temperature | No heating |
| Deposition rate | 7nm/min |
| Distance between target and substrate | 10 <i>cm</i> |

2. Proposed fabrication process flow

The basic process flow of the proposed acoustic device can be summarized as- (i) selection of Si wafers and cleaning using RCA1 (NH_3 (29%): H_2 O $_2$ (30%): DI water = 1:1:5) and RCA2 (HCl(37%): H_2 O $_2$ (30%): DI water = 1:1:6); (ii) fabrication of micro-hole/tunnel for pressure compensation and thereafter, bulk micromachining to release diaphragm; thermal oxidation to grow a thin insulating oxide layer; (iii) deposition of bottom electrode of Au using sputtering and subsequent etching; (iv) deposition of thin PECVD oxide; (v) deposition of piezoelectric layer of ZnO using sputtering and subsequent etching; (vi) again thin PECVD oxide layer deposition along with top electrode of Au deposition by sputtering and then etching followed by RIE to open the pads and finally, anodic bonding with pyrex glass to seal the cavity. Fig. 1 is showing the complete fabrication process flow.

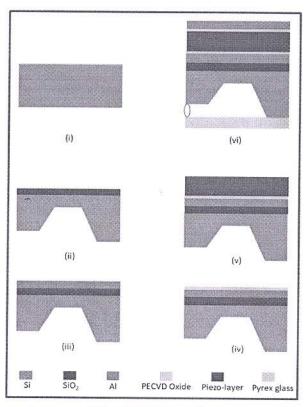


Fig. 1. Fabrication process flow.

3. Result & Discussion

Crystallographic characteristics of the as-deposited and annealed films have been studied using XRD. With the increase in annealing temperature from $200-400^{\circ}$ C, the intensity of peaks has also increased, indicating an enhanced crystallinity (Fig. 2(a)). The high annealing temperature provides energy to the film atoms which enhances their mobility that in turn leads to decrease in defects in the ZnO films. This improves the film quality and hence the enhanced crystallinity [22-23]. Defect free thin film with enhanced crystallinity indicates improved performance of sensor. The (002) peak position of ZnO powder is $2\theta = 34.448^{\circ}$. However, in our deposited ZnO films, there is deviation in the position of the diffraction peak from its powder value. The position shifting indicates a decrease in strain [24-25]. Also, FWHM decreases with the annealing temperature.

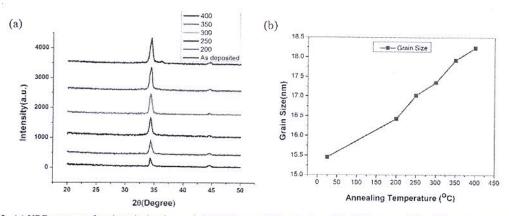


Fig. 2. (a) XRD patterns of as-deposited and annealed ZnO films and (b) grain size of ZnO films annealed at different temperatures.

Figures 2–5 are showing the effect of annealing temperature on various microstructural parameters such as grain size (S), dislocation density (D) and stress (σ). Grain size (S) is calculated using the Scherer formula [26]:

$$S = 0.94 \lambda B \cos \theta \qquad (1)$$

Where λ is the wavelength of the X-ray used in XRD measurements and it is equal to 0.154056nm, B is the full width at half maxima (FWHM) and θ is the Bragg diffraction angle. Therefore, dislocation density can be calculated using the relation [23]:

$$D = \frac{1}{S^2} \tag{2}$$

Finally, the film stress can be calculated from the relation [23, 27]:

$$\sigma = -233 \left(\frac{c}{c - c_o} \right) \tag{3}$$

Here, c is the lattice parameter of the deposited ZnO films and c_o is the unstrained lattice parameter for powder ZnO $(c_o = 0.5205nm)$ [28]. c can be obtained using the relation [23]:

$$c = \frac{\lambda}{\sin \theta} \qquad (4)$$

Grain size increased from 15.46nm to 18.24nm as shown in Fig. 2(b). In the graph, the value at 25° C indicates the grain size of as-deposited ZnO (i.e. room temperature deposited) film. The increase in grain size is due to the recrystallization of the thin film [29]. Surface roughness for an as-deposited ZnO film was high $\sim 4.42nm$ as shown in Fig. 3(a). However, when the annealing temperature was increased upto 350° C, the rms roughness became very low ~ 2.67 nm (Fig. 3(b)). On further increasing the annealing temperature to 400° C, the value rms roughness increased to 3.79nm (Fig. 4).

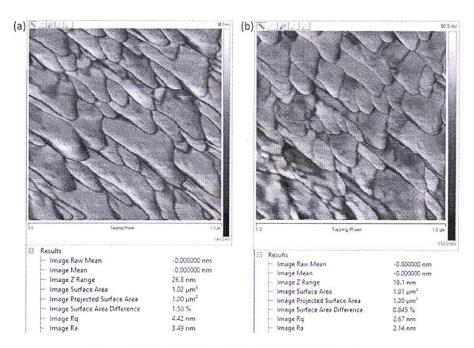


Fig. 3. AFM images of (a) as-deposited and (b) annealed ZnO film.

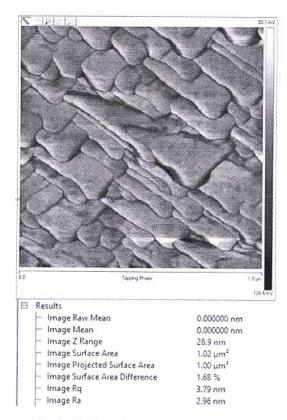


Fig. 4. AFM image of ZnO film annealed at 40 0°C.

The reduction in the roughness which takes place initially with the increase in annealing temperature can occur due to the island coalescence process induced from thermal treatment [23, 30]. Sharma et al. [23] argued that due to the presence of some Frenkel defects such as Zn interstitials and oxygen vacancies at the grain boundaries of deposited films, the coalescence process gets enhanced to make larger grains at higher annealing temperature. However, roughness increases with additional increase in annealing temperature due to further grain growth [22, 26]. The low amount of roughness at 350° C is indicative of smaller acoustic losses in ZnO thin film which is an essential characteristic for acousto-electric applications [31].

Dislocation density has shown a decreasing trend with increasing annealing temperature as depicted in Fig. 5(a). Dislocation density for the as-deposited film is $4.186 \times 10^{-3} \, n \, m^{-2}$. However, it decreases to a value of $3.704 \times 10^{-3} n \, m^{-2}$ at the annealing temperature of $200\,^{\circ}C$ and further decreases to of $3.006 \times 10^{-3} n \, m^{-2}2$ at the temperature of $400\,^{\circ}C$. They opined that due to the movement of Zn interstitials towards the grain boundaries leads to the reduced concentration of lattice imperfections. This may cause the reduction in dislocation density at higher annealing temperature. Lower dislocation density indicates better crystalline quality of the deposited thin film. For simplicity, the as-deposited values are not shown in graphs of dislocation density and stress.

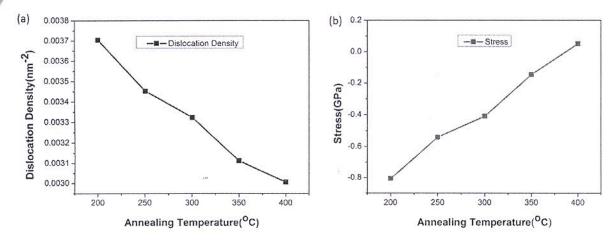


Fig. 5. (a) Dislocation density and (b) stress as functions of annealing temperature

Film stress decreases from the as-deposited film value of -1.0036GPa to -0.80516GPa at the annealing temperature of $200^{\circ}C$. It further decreases to -0.14615GPa at the annealing temperature of $350^{\circ}C$ as shown in Fig.5(b). The negative sign indicates the presence of compressive stress. At lower annealing temperature, the stress is compressive in nature. The presence of the compressive stress in the as-deposited ZnO films provides a driving force for grain growth during post-deposition annealing [24]. As annealing temperature increases, the film has started to show tensile nature of the stress and at the temperature of $400^{\circ}C$, it shows a low tensile stress of 0.050798GPa $\cong 50.8$ kPa. The compressive nature of the stress at lower temperature is due to presence of Zn interstitials. On the other hand, the tensile nature exhibited by the film at higher annealing temperature is due to the domination of oxygen vacancies in the lattice of ZnO crystallites [23]. High crystallinity of ZnO thin films with reduced strain and stress facilitates its use in acoustic sensors and in many optoelectronic applications.

4. Conclusion

ZnO thin films of thickness $2.5\mu m$ were deposited using RF sputtering technique. All the films were annealed at temperatures ranging from $200-400^{\circ}$ C in steps of 50° C and effect of changing temperature upon different microstructural properties such as grain size, surface roughness, dislocation density and stress were studied. All deposited films exhibited a strong (002) crystallographic orientation in XRD measurements and an increase in the intensity of (002) peak as well as increase in grain size with temperature, indicating enhanced crystallinity. Also, dislocation density decreases. Surface roughness has been determined from AFM measurements and it exhibited a very low value of ~ 2.67 nm at the temperature 350° C. Stress was also found to decrease with temperature. Moreover, nature of stress for deposited ZnO thin films got shifted from compressive to tensile as annealing temperature has increased from $200^{\circ}C$ to 400° C. A nearly stress free film can be obtained at an optimized temperature of 350° C. Therefore, acoustic sensors fabricated on annealed stress free ZnO thin film can achieve high sensitivity. They can be utilized in the detection of sound pressure levels in launch vehicles, jet engines etc.

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